Total No. of Questions : 09

B.Voc. (SD)   (Sem.–5)
INTRODUCTION TO MICROPROCESSORS
Subject Code : 7.GV.04
M.Code : 91082
Date of Examination : 19-12-2022

Time : 3 Hrs.   **Max. Marks : 30**

INSTRUCTIONS TO CANDIDATES :
1. **SECTION-A is COMPULSORY consisting of TEN questions carrying ONE marks each.**
2. **SECTION-B contains FIVE questions carrying TWO and HALF marks each and students have to attempt any FOUR questions.**
3. **SECTION-C contains THREE questions carrying FIVE marks each and students have to attempt any TWO questions.**

SECTION-A

1. **Write briefly :**

   a) Finite State Machine

   b) Single Cycle Processor

   c) Virtual Memory

   d) I/O Devices

   e) Addressing Modes

   f) ALU

   g) Test Benches

   h) Memory Map

   i) Address Bus

   j) Assembly Language

SECTION-B

2. What is the concept of pipelining, Explain in detail.

3. Explain the significance and use of cache memory in detail.

4. Compare and contrast sequential logic and combinational logic with help of examples.

5. Write a note on Number Systems.

6. Describe Memory-Mapped I/O and its various components.

SECTION-C

7. Describe in detail the memory system and its various categories.

8. Explain the Control and Datapath design for single cycle processor. Give necessary illustrations and details.

9. Write notes on Structural modelling and parameterized modules.

NOTE : Disclosure of Identity by writing Mobile No. or Making of passing request on any page of Answer Sheet will lead to UMC against the Student.