Roll No. | | | | | | | | | | | | Total No. of Pages : 02

Total No. of Questions : 18

B.Tech.(CSE)/(Electronics & Computer Engg.)/(IT) (2011 Onwards)

(Sem.–4)

MICROPROCESSOR & ASSEMBLY LANGUAGE PROGRAMMING

Subject Code : BTCS-404

Paper ID : [A1186]

Time : 3 Hrs. Max. Marks : 60

INSTRUCTION TO CANDIDATES :

1. **SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.**
2. **SECTION-B contains FIVE questions carrying FIVE marks each and students have to attempt any FOUR questions.**
3. **SECTION-C contains THREE questions carrying TEN marks each and students have to attempt any TWO questions.**

SECTION-A

Explain the following :

1. Interpreter
2. Status Register
3. Flags
4. 2-byte Instruction
5. TRAP
6. PUSH Instruction
7. Emulator
8. Latch Signal
9. DAD Instruction
10. OUT Instruction

SECTION-B

11. What do you understand by subroutine? Explain.

12. Describe serial and parallel data transfer techniques.

13. Explain any 5 addressing modes.

14. Differentiate between instruction, machine and clock cycle.

15. Write an assembly language program to swap two numbers.

SECTION-C

16. Explain the architecture and pin diagram of 8085.

17. Explain how stepper motor can be interfaced with 8085.

18. Write a detailed note on DMA.