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BCA (Sem-4) **OPERATING SYSTEMS**

Subject Code: UGCA-1923

M.Code: 79727

Time: 3 Hrs.

Date of Examination: 02-06-2023

Max. Marks: 60

INSTRUCTIONS TO CANDIDATES :

SECTION-A IS COMPULSORY consisting of TEN questions carrying TWO marks

SECTION-B contains SIX questions carrying TEN marks each and students have to attempt any FOUR questions.

SECTION-A

Write Briefly : I.

A. List and explain the functions and services of an operating system.

5. Defend timesharing differ from multiprogramming?

- Te. Explain process intes with state transition diagram. Also explain PCB with a neat diagrum.
- What are the uses of job queues, ready queues and device queues?
- e. Discuss the difference between symmetric and asymmetric multiprocessing
- f. Distinguish between hard real time systems and soft real time systems.
- g. Can traps be generated intentionally by a user program? If so, for what purpose?
- What are the five major activities of an operating system with regard to process management?
- What are the three major activities of an operating system with regard to memory management?
- What is distributed operating system? Explain its characteristics and architecture.

 Consider the following set of process circles time and burst time, larger priority number has a higher priority.

Jobs	Arrival Time ms Burst Time ms Priority	_
J1	0 6 roomy	-4
J2	3 5 3	
ß	3 3	
J4	5 5 1	

Draw the Gantt chart circulate waiting time and turnaround time using :

- a) FCFS
- b) Preemptive priority scheduling algorithm,
- Consider the methods used by processes P1 and P2 for accessing their critical sections whenever needed, as given below. The initial values of shared Boolean variables S1 and S2 are randomly assigned.

Method used by P_1 While $(S_1 = S_2)$; Critical Section $S_1 = S_2$	Method used by P ₁ While (S ₁ != S ₂); Critical Section S ₂ =- S ₁

Explain by taking different values of S₁ and S₂ that above meine and progress.

- 4. Consider a reference string: 4,7,6, 1, 7, 6, 1,2, 7, 2. The number of frames in the memory is 3. Find out the number of page faults respective to:
 - a) Optimal Page Replacement Algorithm
 - b) FIFO Page Replacement Algorithm
 - c) LRU Page Replacement Algorithm.
- 5. A certain computer system has the segment of paging architecture for virtual memory. The memory is byte addressable. Both virtual and physical address spaces contain 216 bytes each. The virtual address space is divided into 8 non-overlapping equal size segments. The Memory Management Unit (MMU) has a hardware segment table, each entry of which contains the physical address of the page table for the segment. Page tables are stored in the main memory and consists of 2 byte page table entries. What is the minimum page size in bytes 50 that the page table for a segment requires at most one page to store it?