

Roll No.

Total No. of Pages : 02

Total No. of Questions : 07

BCA (Sem.-2)

COMPUTER SYSTEM ARCHITECTURE

Subject Code : UGCA-1908

M.Code : 77416

Date of Examination : 21-11-2023

Time : 3 Hrs.

Max. Marks : 60

INSTRUCTIONS TO CANDIDATES :

1. SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
2. SECTION-B contains SIX questions carrying TEN marks each and students have to attempt any FOUR questions.

SECTION-A

- 1. Write briefly :**
- a) What are the various units of Digital Computer?
 - b) Explain AND and NOT gate with example.
 - c) What is Bus?
 - d) What is Shift register?
 - e) What is JK flip flop, explain with example?
 - f) What is register?
 - g) What are USB interfaces?
 - h) What are the various types of operations required for instructions?
 - i) What are memory reference instructions?
 - j) What are K-Maps?

SECTION-B

2. Differentiate between RISC and CISC Architecture.
3. Draw and explain Full Adder and Full subtractor with example.
4. Explain Implementation of Boolean equations with Multiplexer.
5. Draw and explain D Flip flop and T Flip Flop.
6. Discuss in detail, the basic concepts of instructions and its executions.
7. Explain the three types of Buses in Computer architecture.

NOTE : Disclosure of Identity by writing Mobile No. or Making of passing request on any page of Answer Sheet will lead to UMC against the Student.