Roll No. ☐☐☐☐☐☐☐☐☐☐☐☐☐☐☐   Total No. of Pages : 02

Total No. of Questions : 07

BCA (2013 & Onwards) (Sem.–2)
COMPUTER SYSTEM ARCHITECTURE
Subject Code : BSBC-204
M.Code : 10053

Time : 3 Hrs.   Max. Marks : 60

INSTRUCTION TO CANDIDATES :
1. **SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.**
2. **SECTION-B contains SIX questions carrying TEN marks each and a student has to attempt any FOUR questions.**

SECTION-A

1) **Answer briefly :**

   a) Write the use of register transfer language.

   b) What are addressing modes in computer architecture?

   c) Discuss the purpose of control bus.

   d) What is network port?

   e) What is use of handshaking?

   f) List advantages of hardwired control.

   g) Explain about LRU replacement algorithm.

   h) What steps through which the processor handles the interrupts?

   i) Write a short note on associate mapping.

   j) List advantages of DMA.

SECTION-B

2. What is the Flynn's classification? Discuss SISD in computer architecture.

3. Explain the micro operations. Give the examples of logic and shift operations.

4. What are instruction formats? Explain direct and indirect address instruction with suitable example.

5. Give the introduction of Control units. Explain the micro programmed control unit.

6. Show the basic architecture of the Mobile device. Give some examples of it.

7. List the advantage of cache in computer architecture. Discuss the process of writing data into cache.

NOTE : Disclosure of Identity by writing Mobile No. or Making of passing request on any page of Answer Sheet will lead to UMC against the Student.